Amendments to the Claims

Please amend the claims as set forth in the following listing. This listing of claims will replace all prior versions, and listings, of claims for the present application:

1. (Previously Presented) A method of isolating a telephone line, comprising:

providing modem circuitry;

providing system side line isolation circuitry;

integrating the modem circuitry and system side line isolation circuitry within a single integrated circuit, the single integrated circuit configured to communicate through an isolation barrier;

providing an asynchronous serial port on the single integrated circuit, the asynchronous serial port being configured to communicate with a system-side external circuit;

configuring the single integrated circuit to use the asynchronous serial port to transfer data formatted with HDLC framing between the single integrated circuit and the system-side external circuit through the asynchronous serial port;

asynchronously receiving HDLC framed data from system-side external circuit utilizing a pin on the integrated modem and system side line isolation circuitry to indicate when it is ready to receive data from the system-side external circuit; and

treating as end of frame indicator a condition where no HDLC framed data is being received by the integrated modem and system side line isolation circuitry while the pin is indicating that the integrated modem and system side line isolation circuitry is ready to receive data.

2. (Previously Presented) The method of claim 1, further comprising changing the state of the pin to not-ready when an end of frame event has been detected and changing the state of the pin back to ready when the integrated modem and system side line isolation circuitry is again ready to receive HDLC framed data.

3. (Original) The method of claim 1 wherein the asynchronous serial port is a transmit pin of the single integrated circuit.

4. (Original) The method of claim 1 wherein the asynchronous serial port is a receive pin of the single integrated circuit.

5. (Previously Presented) The method of claim 4, further comprising providing a transmit pin of the single integrated circuit, the receive pin and the transmit pins being asynchronous serial pins, the receive pin configured to receive modem information into the single integrated circuit from the external system-side circuit and the transmit pin configured to transmit modem information from the single integrated circuit to the external system-side circuit.

6-29. CANCELED.

30. (Currently Amended) ~~Circuitry~~ A system for transferring data formatted with HDLC framing, comprising:

an integrated modem and line-isolation circuit;

an asynchronous serial pin, the asynchronous serial pin being an input or output pin of the integrated modem and line-isolation circuit;

a pin on the integrated modem and line isolation circuit to indicate when it is ready to receive data from an external circuit; and

means to enable use of the asynchronous serial pin to transfer of data formatted with HDLC framing between the integrated modem and line-isolation circuit and ~~an~~ the external circuit through the asynchronous serial pin;

wherein an end of frame is indicated when a condition occurs where no HDLC framed data is being received by the integrated modem and system side line isolation circuitry while the pin is indicating that the integrated modem and system side line isolation circuitry is ready to receive data.

31. (Currently Amended) The ~~circuitry~~ system of claim 30 wherein the means comprises a control pin providing synchronous modem transmission protocol information.

32. (Currently Amended) The ~~circuitry~~ system of claim 31, wherein the synchronous modem transmission protocol information being end of frame information.

33. (Currently Amended) The ~~circuitry~~ system of claim 31, wherein the synchronous modem transmission protocol information being clear to send information.

34. (Currently Amended) The ~~circuitry~~ system of claim 30 wherein the means comprises providing synchronous modem transmission protocol information at least one bit of words transferred through the asynchronous serial pin.

35. (Currently Amended) The ~~circuitry~~ system of claim 34, wherein the synchronous modem transmission protocol information being end of frame information.

36. (Currently Amended) The ~~circuitry~~ system of claim 30, wherein the state of the pin is configured to be changed to not-ready when an end of frame event has been detected and the state of the pin is configured to be changed back to ready when the integrated modem and system side line isolation circuitry is again ready to receive HDLC framed.

37-46. CANCELED.